

Study of μ COS RTOS on Low Cost Microcontroller

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Abstract: Numbers of RTOS products from various vendors are available in the market, μ C/OS-II a freeware with minimum facility is more popular among the hobbyist, researchers and small embedded system developers. In this project LCD is interfaced with the microcontroller using Serial interface. Four tasks are created using μ C/OS-II RTOS. Task1 is for manage all other tasks from task create to delete. Task2 for reading characters from key board. Task3 for Displaying the entered characters on Hyper Terminal when ENTER key is pressed. Task4 for Displaying message on LCD when SPACE key is pressed. The software tools SDCC Compiler and ATMEL FLIP tool are used for implementation of tasks using μ C/OS-II RTOS on AT89C51ED2 embedded development board. The result obtained indicates that best suitable for small scale industrial automation.

Keywords: SDCC, Keil IDE, μ C/OS-II RTOS, Flash Magic and Embedded Development Board.

I. INTRODUCTION

The human effort needed for implementing μ C/OS-II RTOS is less compared to other RTOS products. Among the available RTOS μ C/OS-II RTOS is suitable for small scale embedded system using various controllers and processors, since it is a freeware and easily available. A micro-kernel operating system (μ C/OS-II RTOS) written by John J Lebrosse is a freeware for peaceful research purpose. The source code is written in C language, which is compliant to ANSI 'C' format. It has about 10,000 lines of source code with well-documented comments in the source code. It is a well-tested source code. The μ C/OS-II RTOS supports pre-emptive scheduling and not efficient with respect to processor utilization.

The scaled-version of μ C/OS-II RTOS on 8051 with multiple tasks uses 4 KB of flash and 512 bytes of RAM. The based on the idea of only placing essential core real-time operating system functions in the kernel is of micro kernel and other functionality is designed in modules that communicate with the kernel via minimal defined interfaces. The results in easy reconfigurable systems without the need to rebuild the kernel in micro kernel. The μ C/OS-II kernel is completely designed in software and is a well-used RTOS for combined set of software and hardware systems. It is described in a monolithic microkernel in the language of ANSI C with a small part in assembly language for context switching.

The objective of this project is to port μ C/OS-II RTOS to 8051 controller and study the performance of multiple tasks. The performance of the 8051 microcontroller respect to different tasks is simulated in software platform and verified in the target hardware. To meet the above objectives, the software tools used for 8051 controller in this work are SDCC (Small device C Compiler) full version and Atmel Flip2.4.2. The μ C/OS-II RTOS is loaded to the system and different tasks are executed.

II. PROPOSED WORK

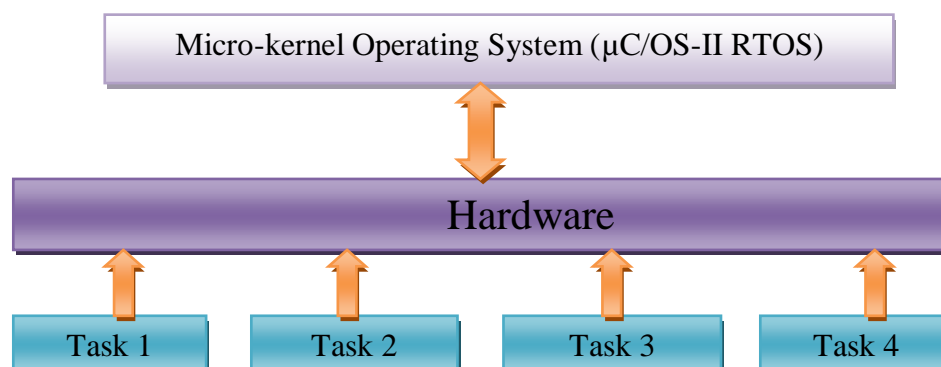


Fig. 1: Block diagram of handling of several tasks



The proposed work shown in figure 1 is implemented on a low-end hardware. The figure 1 shows conceptual view of a scheduler which has to handle several tasks. The role of the scheduler is to handle several tasks by meeting all the deadlines. Several types of tasks like reading a key from a keypad, displaying messages on LCD, displaying messages on Hyper terminal. $\mu\text{C}/\text{OS-II}$ RTOS is implemented on 8051 board.

The SDCC compiler is an improved version of C Compiler which can generate the Hex file with less memory as compared to other compilers. The advantage of using this is thus generated hex file can be downloaded on to microcontroller hence we will get free memory to install $\mu\text{C}/\text{OS-II}$ RTOS on microcontroller.

III. $\mu\text{C}/\text{OS-II}$ RTOS

One of the popular RTOS for the embedded system development is $\mu\text{C}/\text{OS-II}$. For non-commercial use, $\mu\text{C}/\text{OS-II}$ RTOS is also a freeware. Jean J. Labrosse designed it in 1992 and nowadays $\mu\text{C}/\text{OS-II}$ is well developed for a number of applications. It is available from Micrium (www.micrium.com). The $\mu\text{C}/\text{OS-II}$ name is obtained from MCOS. It is also popularly known as $\mu\text{C}/\text{OS-II}$ or MicroCOS or UCOS. Micrium describes portable $\mu\text{C}/\text{OS-II}$, it's able to work as ROM, scalability, preemptibility, with real-time and multitasking kernel. $\mu\text{C}/\text{OS-II}$ has been used in over thousands of applications, including automotive, avionics, electronics, the devices which are used in medical, military purpose, aircraft and aerospace, and networking, and systems-on-a-chip development. $\mu\text{C}/\text{OS-II}$ has an elegant code and it gives good performance ratio with high quality. The USA defence department certified the code for widely use in medical applications and in avionics. It has a precertifiable software component for safety critical systems, including avionics system ADO-178B and EUROCAE ED-12B, medical FDA 510(k) for transportation and IEC 61058 standards is mainly for nuclear systems. Using this RTOS has another advantage. It has full source code availability and has been elegantly and very well documented in the book by its designer. Its code ports on many processors that are commonly used in the designing of embedded systems. $\mu\text{C}/\text{OS-II}$ is real-time kernel with additional support as follows.

1. μC hardware peripherals.
2. $\mu\text{C}/\text{FL}$ (an embedded flash memory loader).
3. $\mu\text{C}/\text{FS}$ (an embedded memory file system).
4. $\mu\text{C}/\text{GUI}$ (an embedded GUI platform).
5. $\mu\text{C}/\text{Probe}$ (monitoring tool).
6. $\mu\text{C}/\text{TCPIP}$ (TCP-IP stack).
7. $\mu\text{C}/\text{CAN}$ (an embedded controller area network bus).
8. $\mu\text{C}/\text{MOD}$ (an embedded modbus).
9. $\mu\text{C}/\text{USB}$ device and $\mu\text{C}/\text{USB}$ host (embedded USB-devices framework).

The Program is written in ANSI 'C' and is verified by the author that it can be compiled in most of the compilers. It is a freeware for peaceful research purposes only. For commercial purposes, royalty has to be paid to the author. It is a scalable operating system. It supports pre-emptive scheduler and lacks to support other schedulers. In this work, scheduler in $\mu\text{C}/\text{OS-II}$ RTOS is used. $\mu\text{C}/\text{OS-II}$ RTOS has 9,900 lines length of code. In this work, it is scaled to about 6,000 lines of code for 8051 controller. The scaling of operating system is performed by configuring the parameters in config.h file. The functions of the RTOS source code which can be configured by a user.

IV. PROCESSOR INDEPENDENT CODE

The processor independent code is the code which is applicable to any processor without any modifications. In $\mu\text{C}/\text{OS-II}$, this code is written in files as

1. OS_CORE.C
2. OS_MBOX.C
3. OS_MEM.C
4. OS_Q.C
5. OS_SEM.C
6. OS_TASK.C
7. OS_TIME.C
8. $\mu\text{C_II.C}$
9. $\mu\text{C_II.H}$

$\mu\text{C}/\text{OS-II}$ header (included in master) and C files are $\mu\text{C_II.C}$ and $\mu\text{C_II.H}$ respectively. The files for the RTOS core, timer and task files are OS_CORE.C, OS_TIME.C and OS_TASK.C respectively. The files for the memory-partitioning, semaphore, queue and mailbox codes are OS_MEM.C, OS_SEM.C, OS_Q.C and OS_MBOX.C respectively.

V. HARDWARE SETUP OF 8051 BOARD

The hardware board as shown in figure 2 consists of functional blocks such as

1. Power supply
2. 8 LEDs
3. 4x4 Keypad
4. RS-232 port interface
5. 7 segment LED Display
6. 2x16 LCD

The input to power supply is given from a DC adapter of 5 volts. The rectified output of power supply block is given to microcontroller and other peripherals attached to it. LEDs can be programmed by setting the bit zero or one in any of the 8051 I/O ports to turn it ON/OFF. A 2x16 LCD consists of two rows. A keypad consists of 16 keys. A serial port interface is used to communicate between PC and target hardware. This is a serial communication interface and 9600 bps baud rate is used. Figure 2 shows the complete hardware setup used in this work. The 8051 microcontroller and PC interacts with each other using RS-232 port interface. In PC, the Windows operating system is used along with SDCC and Atmel Flip.

VI. RESULTS

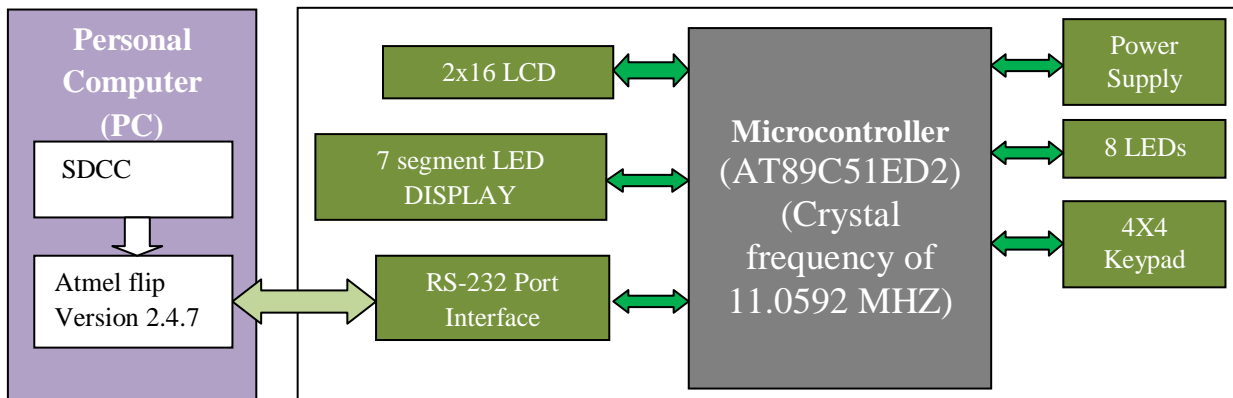


Fig. 2: Complete hardware setup of 8051 board

The program is compiled and loaded in to the AT89C51ED2 controller board. The corresponding results from the hardware are analyzed using theoretical approach and respective captured outputs are as shown below.

```

Microsoft Windows XP [Version 5.1.2600]
(C) Copyright 1985-2001 Microsoft Corp.

C:\Documents and Settings\admin\CD\
C:\>cd serial_lcd
C:\serial_lcd>makefile
C:\serial_lcd>asx8051 -logy os_cpu_asm.ncs51
C:\serial_lcd>sdcc -c -U -mncs51 --opt-code-speed --no-peep --model-large os_task.c
* C:\PROGRAM\1\SDCC\bin\sdccpp.exe -nostdinc -Wall -std=c99 -obj-ext=.rel -DSGCC_M
ODEL_LARGE -DSGCC=260 -DSGCC_ncs51 -D_ncs51 -I"C:\Program Files\SDCC\bin\.\inc
lude\ncs51" -I"C:\Program Files\SDCC\bin\.\include" "os_task.c"
* C:\PROGRAM\1\SDCC\bin\asx8051.exe -plosgff "os_cpu.c.asm"
C:\serial_lcd>sdcc -c -U -mncs51 --opt-code-speed --no-peep --model-large os_sen
.c
* C:\PROGRAM\1\SDCC\bin\sdccpp.exe -nostdinc -Wall -std=c99 -obj-ext=.rel -DSGCC_M
ODEL_LARGE -DSGCC=260 -DSGCC_ncs51 -D_ncs51 -I"C:\Program Files\SDCC\bin\.\inc
lude\ncs51" -I"C:\Program Files\SDCC\bin\.\include" "os_sen.c"
* C:\PROGRAM\1\SDCC\bin\asx8051.exe -plosgff "os_sen.asm"
C:\serial_lcd>sdcc -c -U -mncs51 --opt-code-speed --no-peep --model-large os_mem
.c
* C:\PROGRAM\1\SDCC\bin\sdccpp.exe -nostdinc -Wall -std=c99 -obj-ext=.rel -DSGCC_M
ODEL_LARGE -DSGCC=260 -DSGCC_ncs51 -D_ncs51 -I"C:\Program Files\SDCC\bin\.\inc
lude\ncs51" -I"C:\Program Files\SDCC\bin\.\include" "os_mem.c"
* C:\PROGRAM\1\SDCC\bin\asx8051.exe -plosgff "os_mem.asm"
C:\serial_lcd>sdcc -c -U -mncs51 --opt-code-speed --no-peep --model-large serial
_lcd.c
* C:\PROGRAM\1\SDCC\bin\sdccpp.exe -nostdinc -Wall -std=c99 -obj-ext=.rel -DSGCC_M
ODEL_LARGE -DSGCC=260 -DSGCC_ncs51 -D_ncs51 -I"C:\Program Files\SDCC\bin\.\inc
lude\ncs51" -I"C:\Program Files\SDCC\bin\.\include" "serial_lcd.c"
serial_lcd.c:280:1: warning: no newline at end of file
* C:\PROGRAM\1\SDCC\bin\asx8051.exe -plosgff "serial_lcd.asm"
C:\serial_lcd>sdcc -c -U -mncs51 --opt-code-speed --no-peep --model-large init.c
* C:\PROGRAM\1\SDCC\bin\sdccpp.exe -nostdinc -Wall -std=c99 -obj-ext=.rel -DSGCC_M
ODEL_LARGE -DSGCC=260 -DSGCC_ncs51 -D_ncs51 -I"C:\Program Files\SDCC\bin\.\inc
lude\ncs51" -I"C:\Program Files\SDCC\bin\.\include" "init.c"
* C:\PROGRAM\1\SDCC\bin\asx8051.exe -plosgff "init.asm"
C:\serial_lcd>sdcc -U -mncs51 --model-large --xran-loc 0x0000 --code-loc 0x0000
serial_lcd.rel init.rel os_cpu_asm.rel os_task.rel os_time.rel os_core.rel os_cp
u.c.rel os_sen.rel os_mem.rel
* C:\PROGRAM\1\SDCC\bin\aslink.exe -nf "serial_lcd.lnk"
C:\serial_lcd>packlhx serial_lcd.ihx 1serial_lcd.hex
packlhx: read 1196 lines, wrote 772: OK.
C:\serial_lcd>_

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Fig. 3: Screen shot of project compilation in SDCC

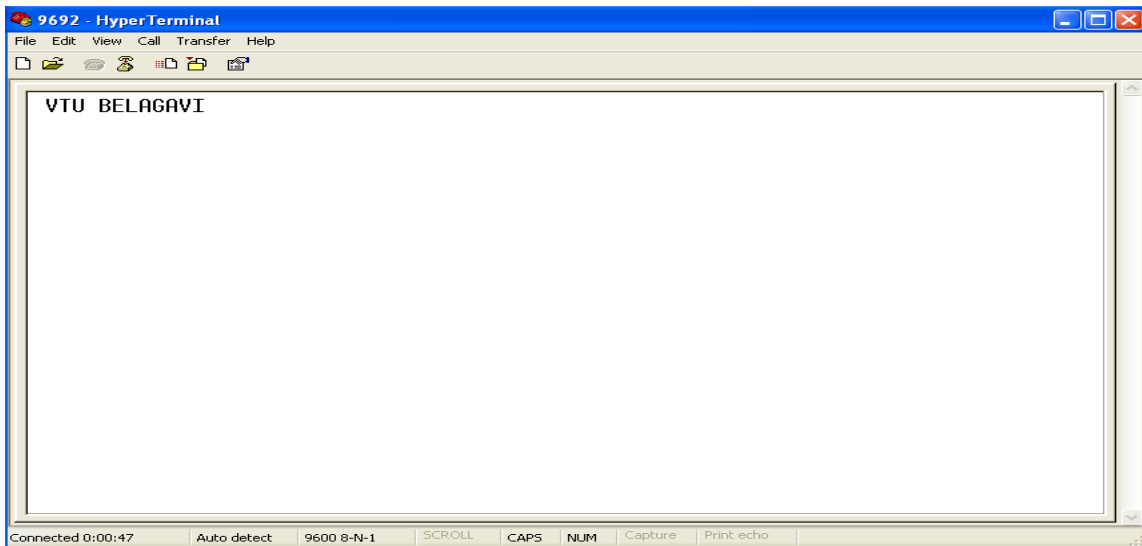


Fig. 4: Screen shot of output on HyperTerminal



Fig. 5: Screen shot of output on LCD display

VII. CONCLUSION AND FUTURE WORK

The SDCC compiler is advanced version of C Compiler which can generate the Hex file with less memory as compared to other compilers. The benefits of using this is thus generated hex file can be downloaded on to microcontroller hence we will get free memory to install μ COS-II RTOS on microcontroller. The results also show the ability to quickly and easily implement various other tasks by reconfiguring the microcontroller OS-II header file through the μ C/OS-II RTOS.

The above work can be extended by designing an embedded system with GUI for the following

1. Toys with remote
2. Automated security system in banks to handle ATM transactions.
3. Photo-framer application
4. Encryptor/Decryptor to handle IP packets

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BIOGRAPHIES



Pundaraja was born on 8th October 1994, in Gulbarga, Karnataka, India. He studied his bachelor degree in Electronics and Communication Engineering from Maratha Mandal Engineering College, Belagavi, Karnataka, India in 2016 and currently pursuing his masters in Digital Communication and Networking in Dr.Ambedkar Institute of Technology, Bengaluru, Karnataka, India. His research interests include digital communication systems, Embedded systems design and Networking field.



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